



GAU 28110  
IR-1609 (2-1941)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

New York, New York

Daniel M. KINZER

Date: November 14, 2000

Serial No.: 09/292,186

Group Art Unit: 2811

Filed: April 15, 1999

Examiner: S. Hu

For: **P-CHANNEL TRENCH MOSFET STRUCTURE**

Asst. Commissioner for Patents  
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement Office Action mailed November 3, 2000, applicants elect to prosecute the invention of claims 1-13 and 20-22 drawn to a semiconductor device, without traverse.

Claims 16-19 are being retained in this application, pending the possible filing of a divisional application.

A favorable consideration and allowance of claims 1-13 and 20-22 is requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on November 14, 2000

Respectfully submitted,

Samuel H. Weiner

Name of applicant, assignee or  
Registered Representative

Signature

November 14, 2000

Date of Signature

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